

INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Not for submission under 37 CFR 1.99)</i>	Application Number		10882468
	Filing Date		2004-04-12
	First Named Inventor		Mitchell Alsup
	Art Unit		2183
	Examiner Name		George D. Zalepa
	Attorney Docket Number		5500-92000

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W	4	6449714		2002-09-10	Sinharoy	
W	5	6339822		2002-01-15	Miller	
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W	9	6167536		2000-12-26	Moann	
W	10	6357016		2002-03-12	Rodgers et al	
W	11	7003629		2006-02-21	Alsup	
W	12	6345295		2002-02-05	Beardsley et al	
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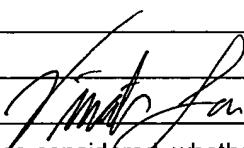
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W	6	Hinton, G., et al., "A 0.18-MUM CMOS IA-32 Processor with a 4-GHZ Integer Execution Unit," IEEE Journal of Solid-State Circuits, Vol. 36, No. 11, November 2001, pages. 1617-1627.	<input type="checkbox"/>
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